

FIG. 1

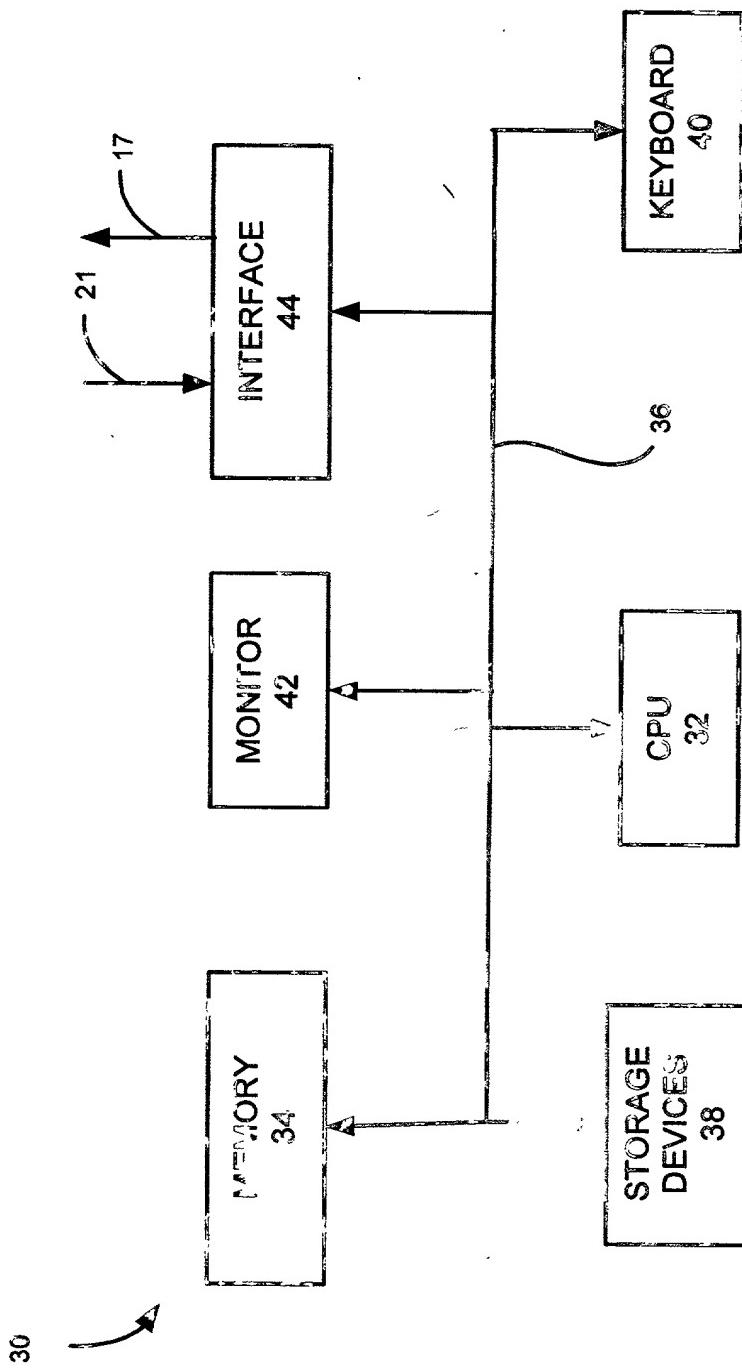


FIG.2

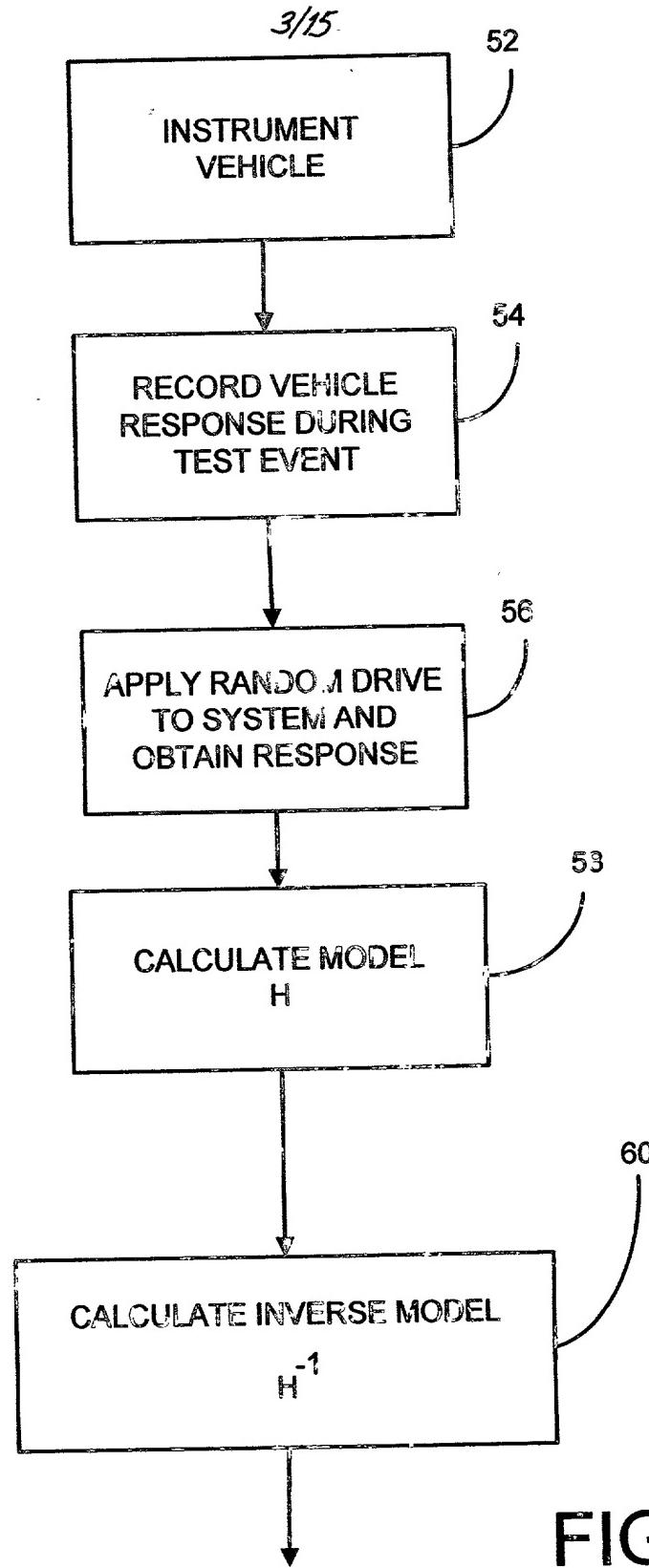


FIG. 3A

FIG. 3B
OR
FIG. 3C

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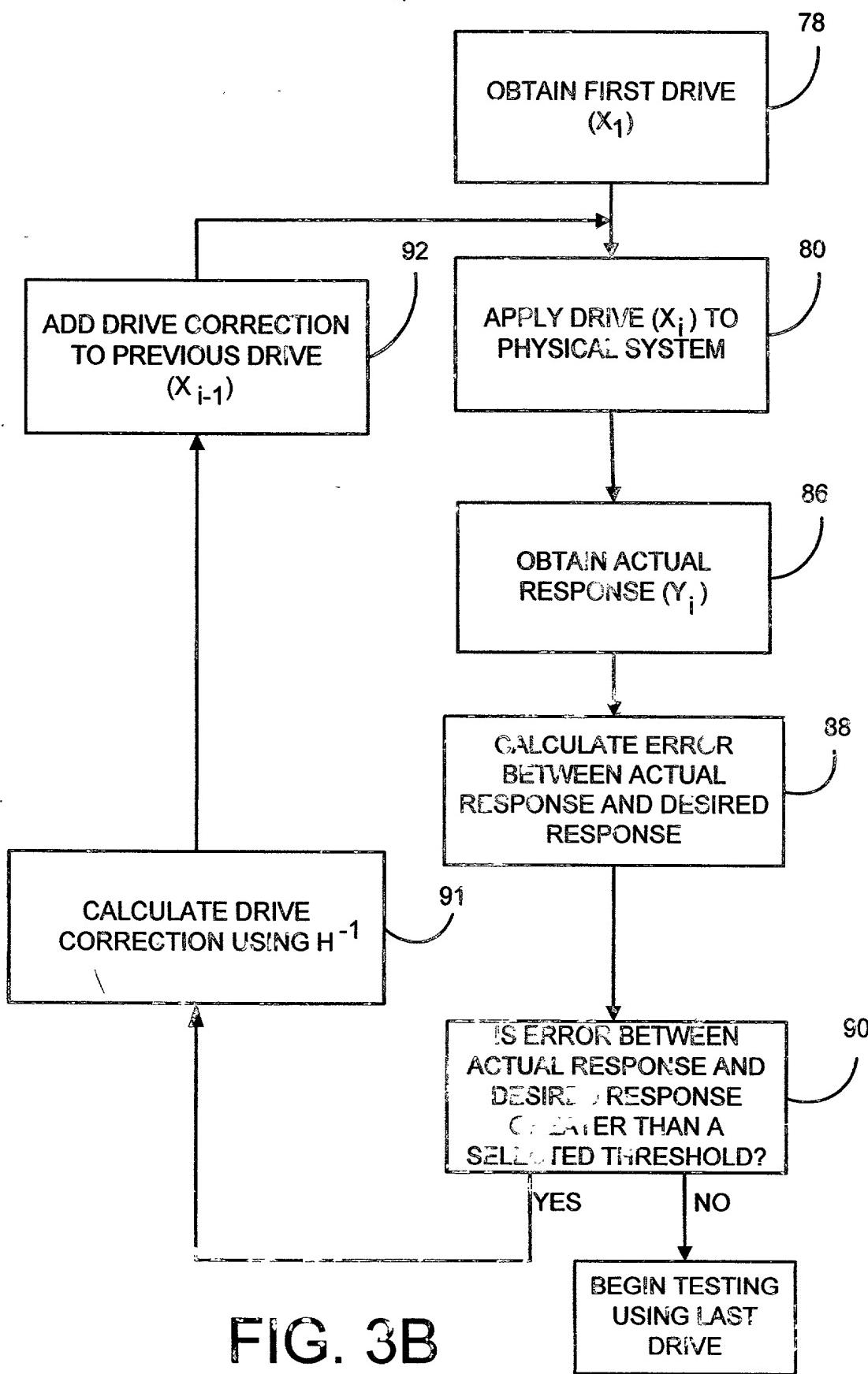


FIG. 3B

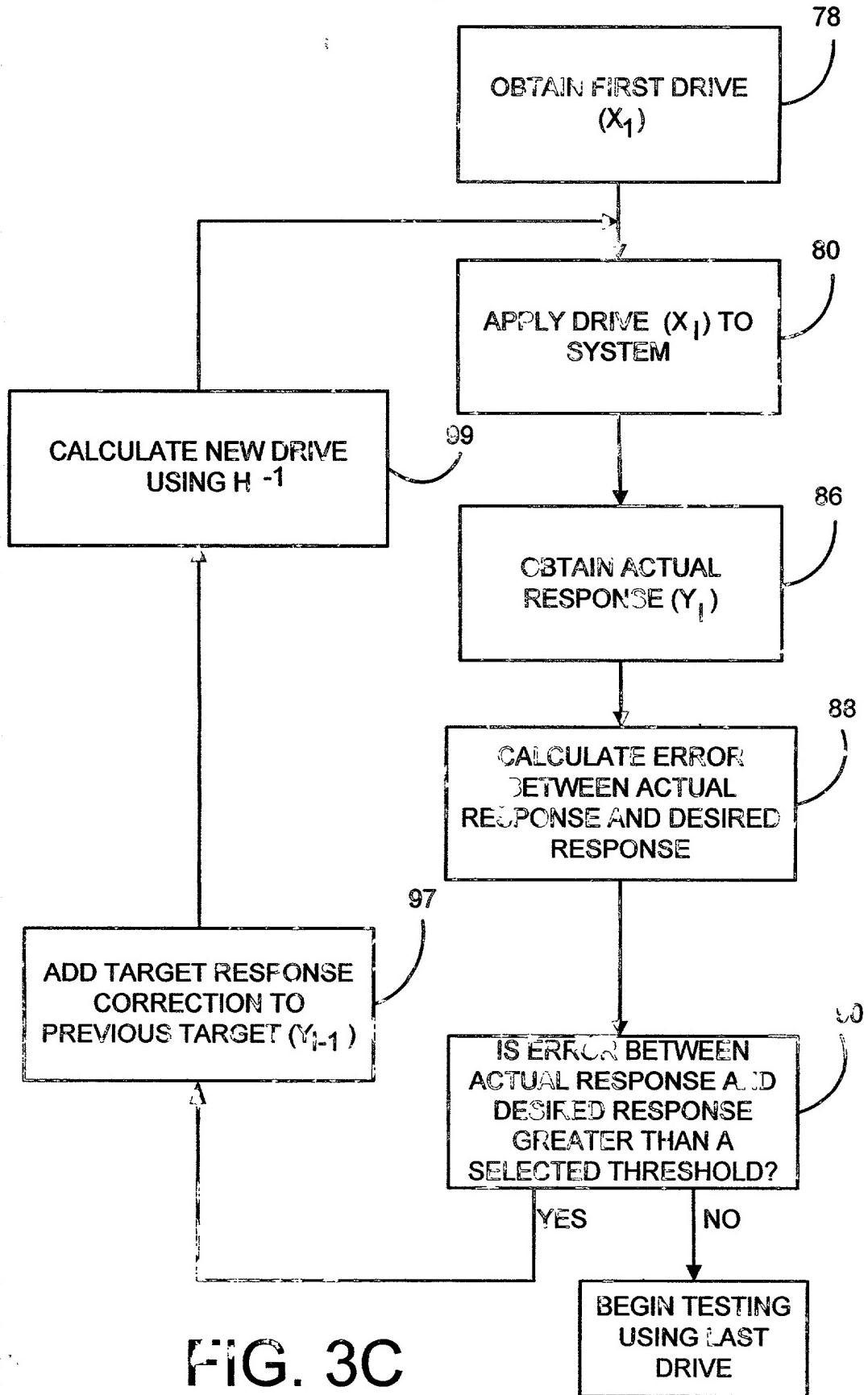


FIG. 3C

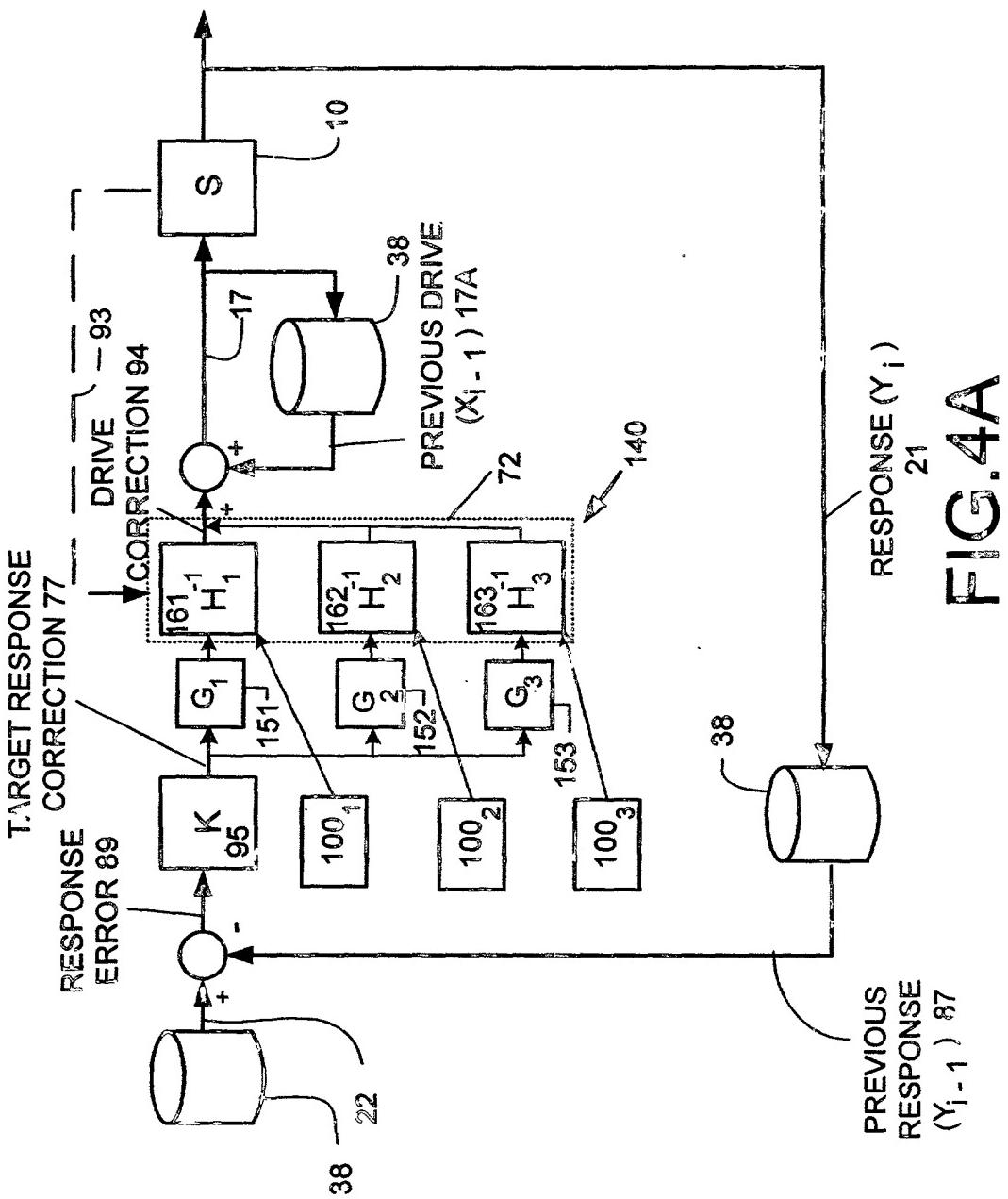


FIG.4A

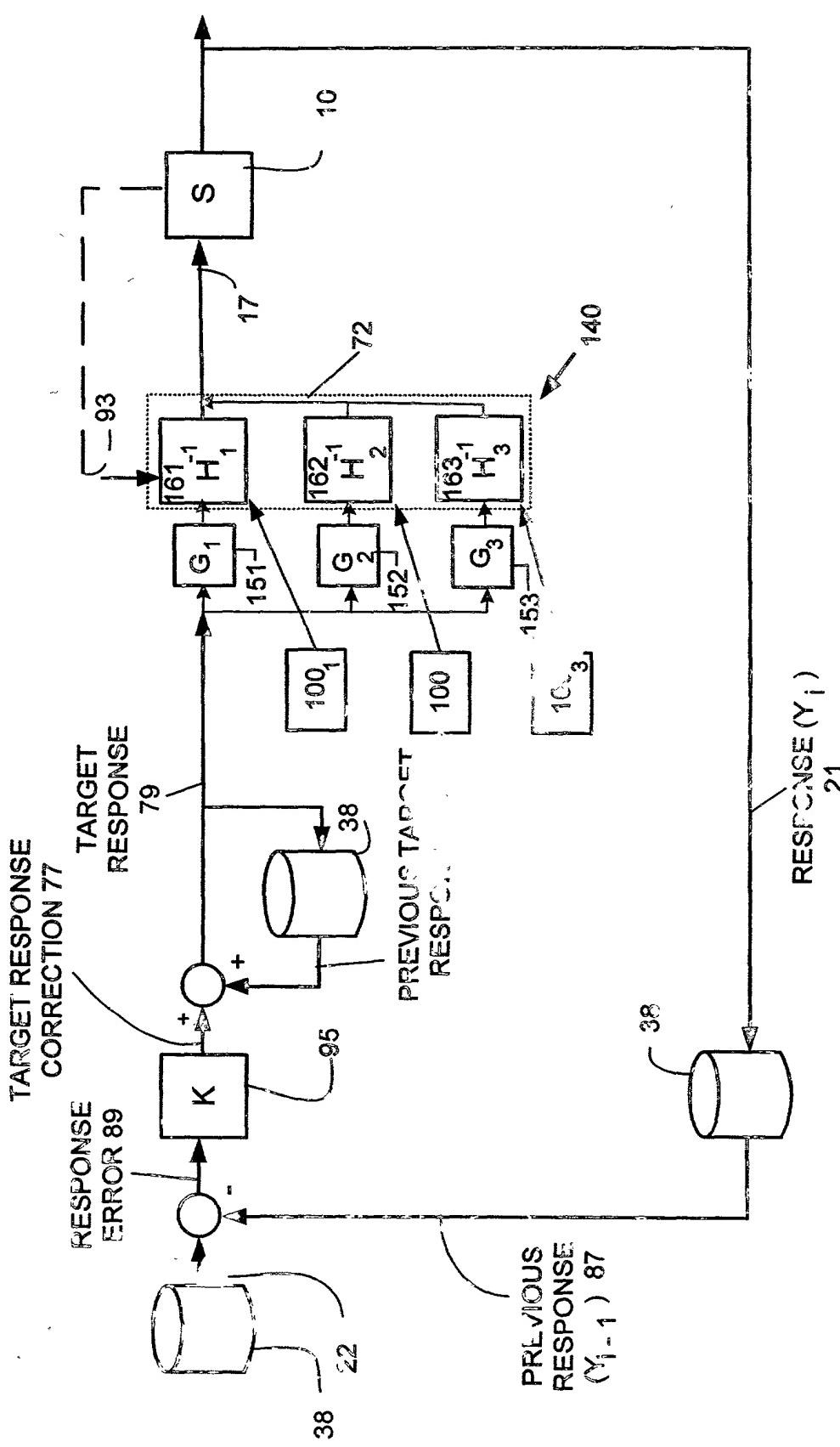


FIG. 4B

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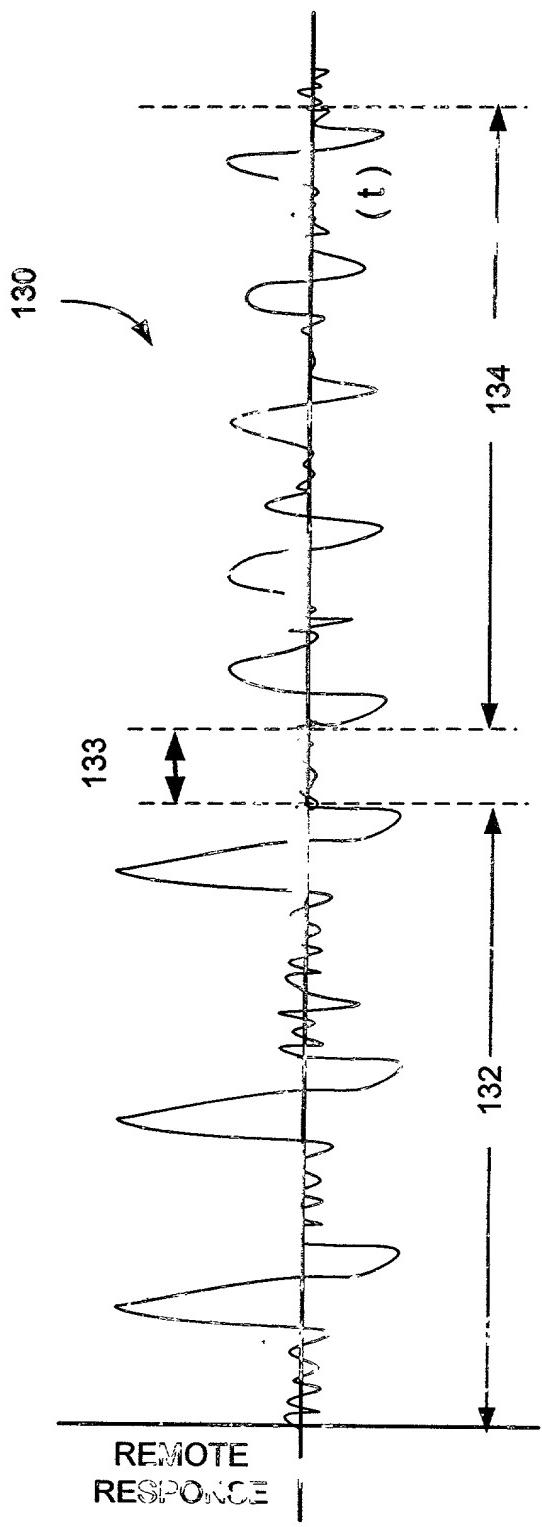


FIG. 5

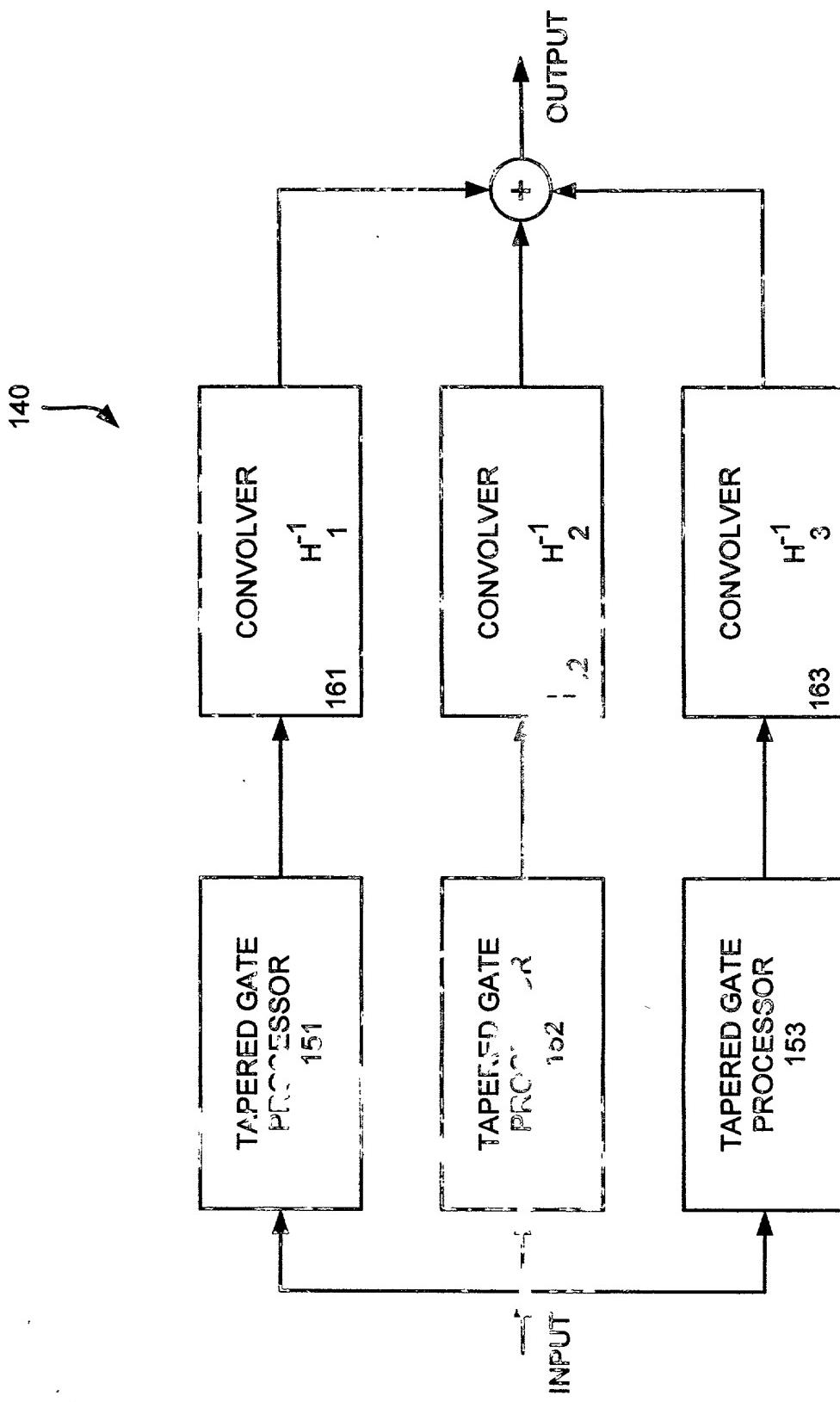


FIG. 6

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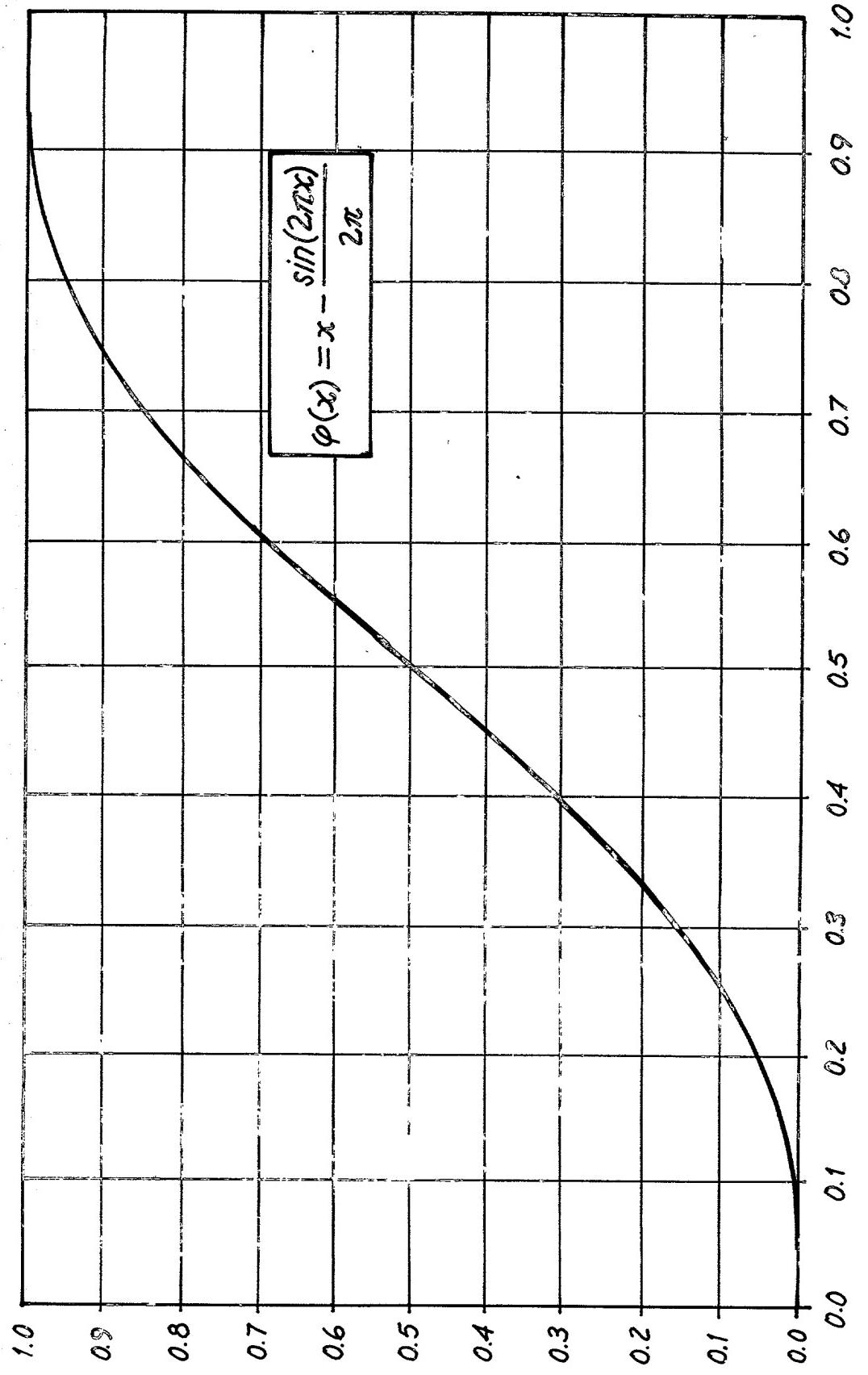
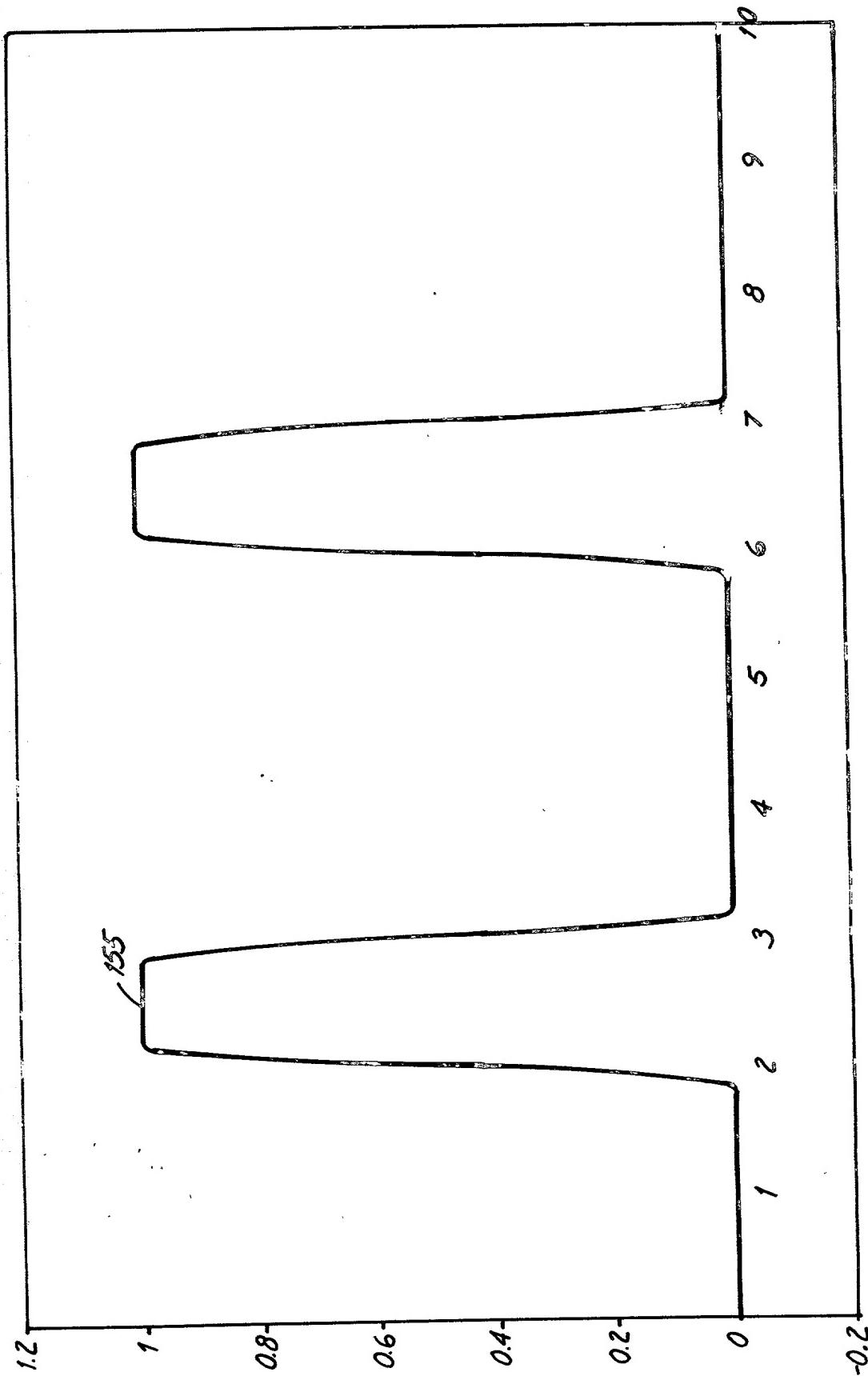


FIG. 7

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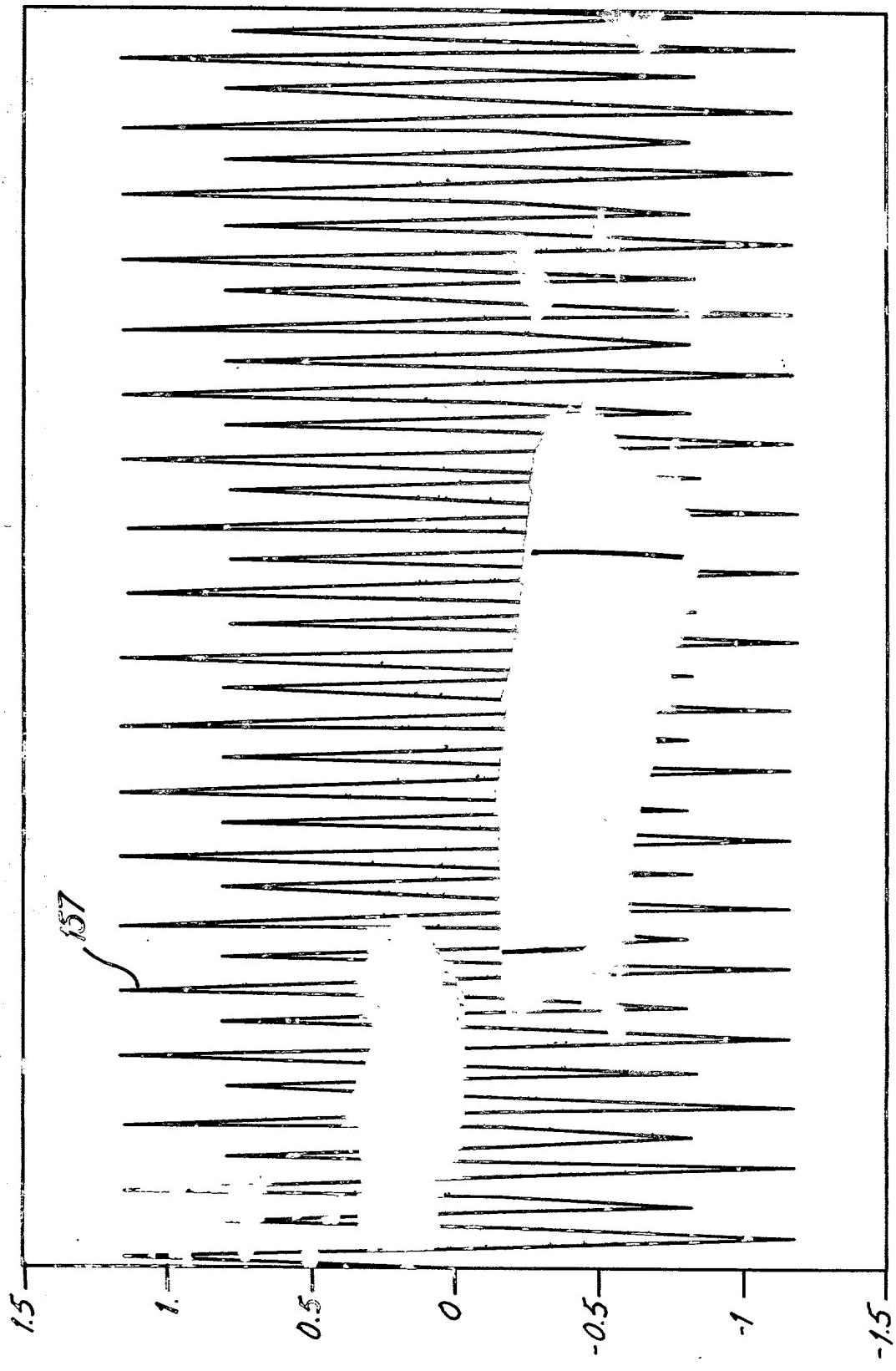


FIG. 9

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GATE INPUT SIGNAL

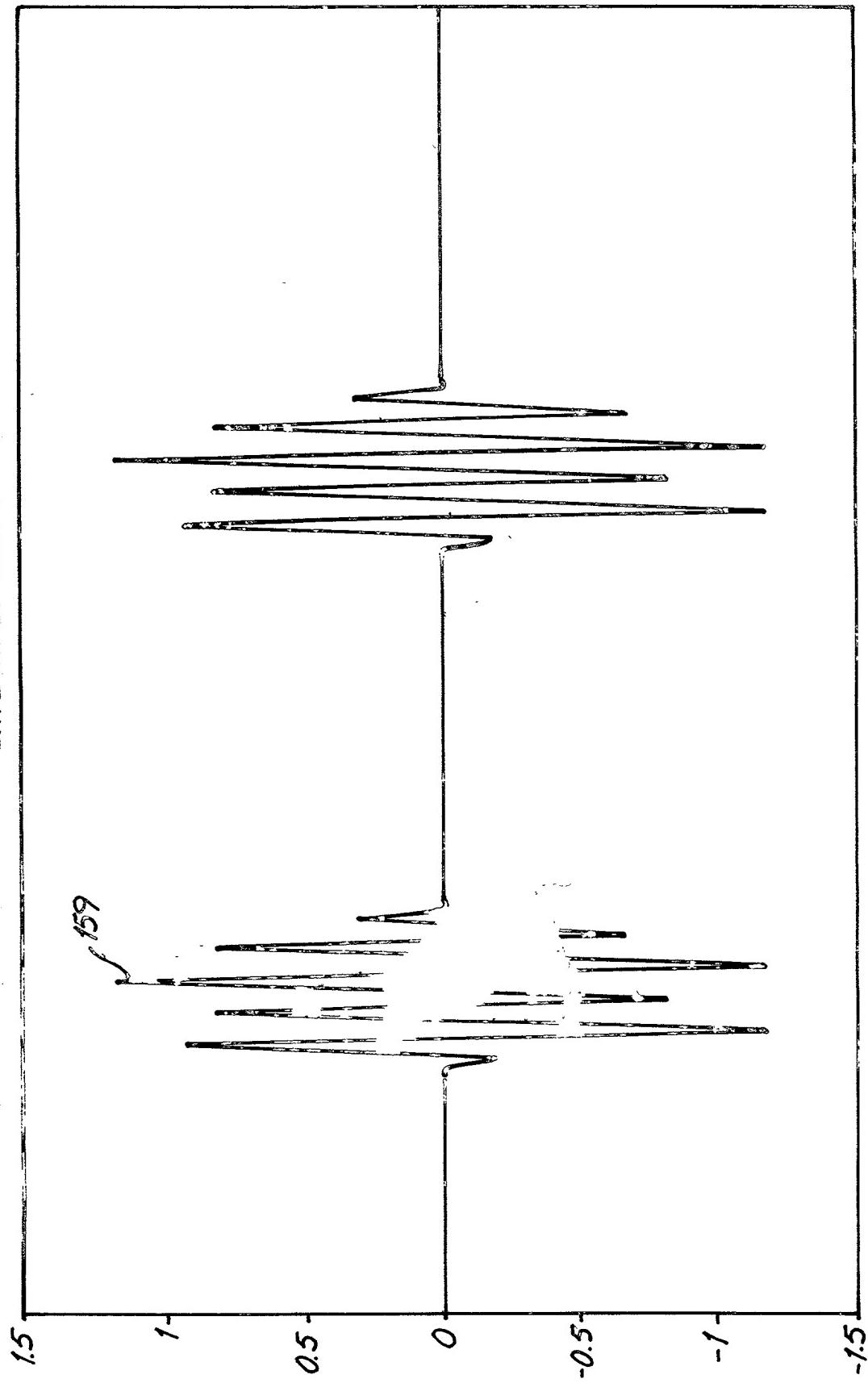


FIG. 16

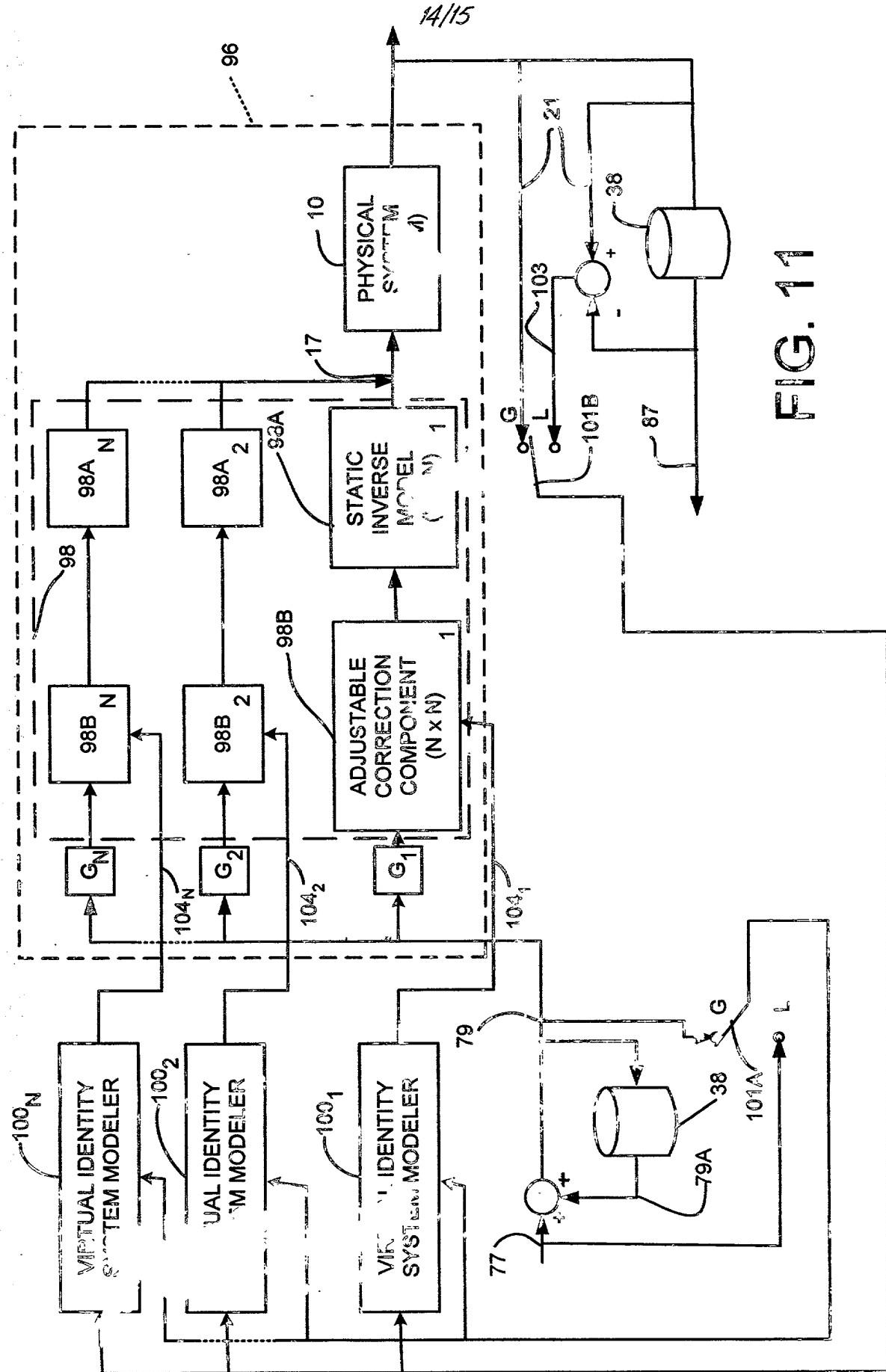


FIG.

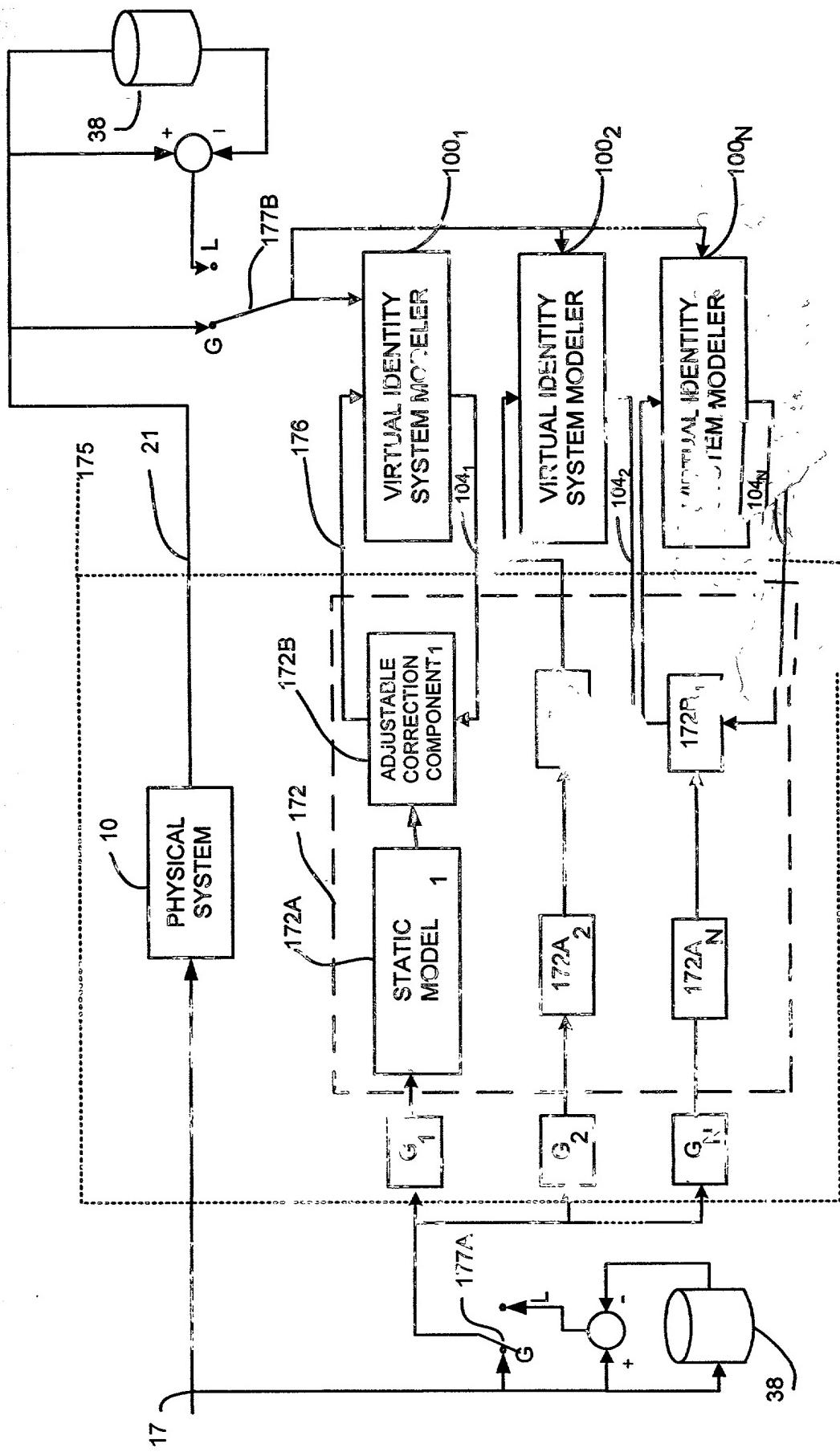


FIG. 12